

FIG. 1

PASSIVE CONFIGURATION

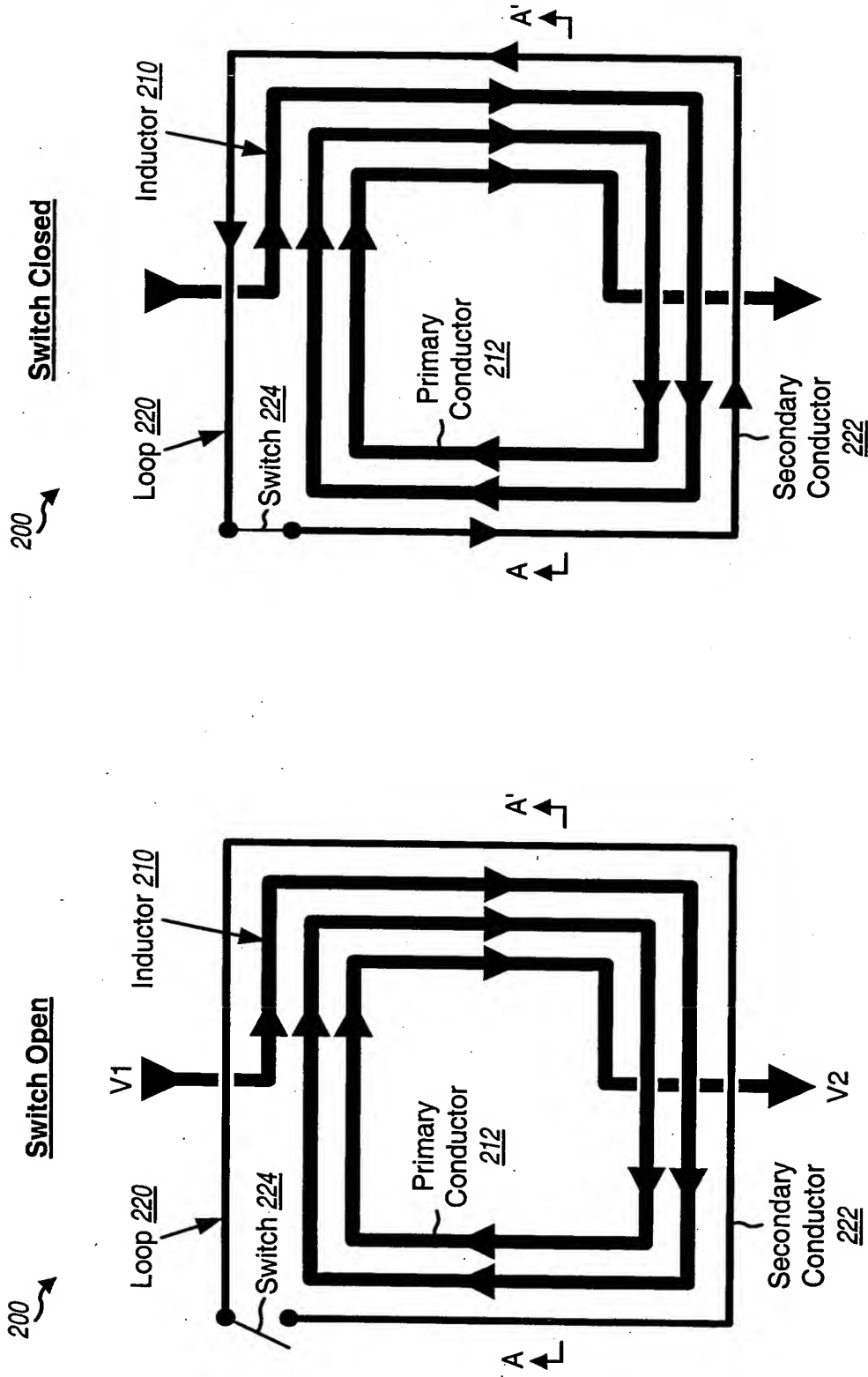


FIG. 2A

FIG. 2B

ACTIVE CONFIGURATION

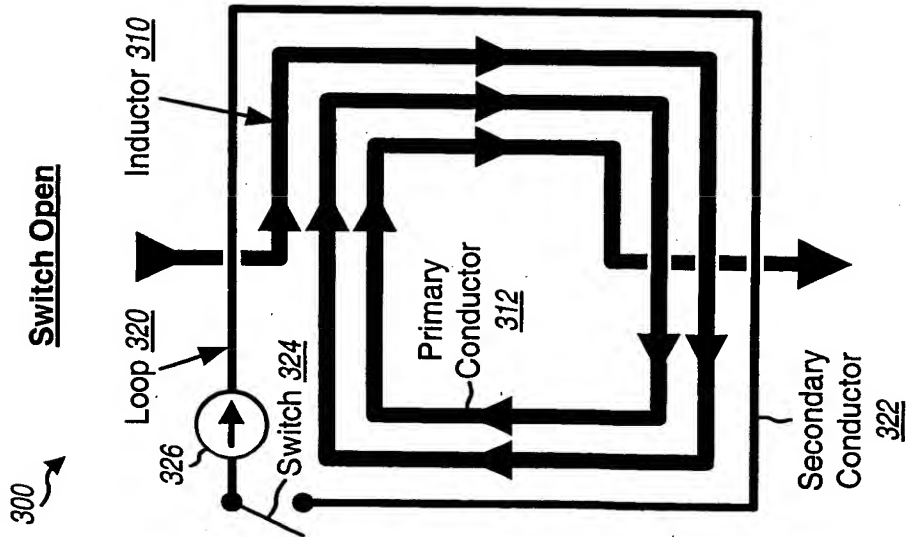


FIG. 3A

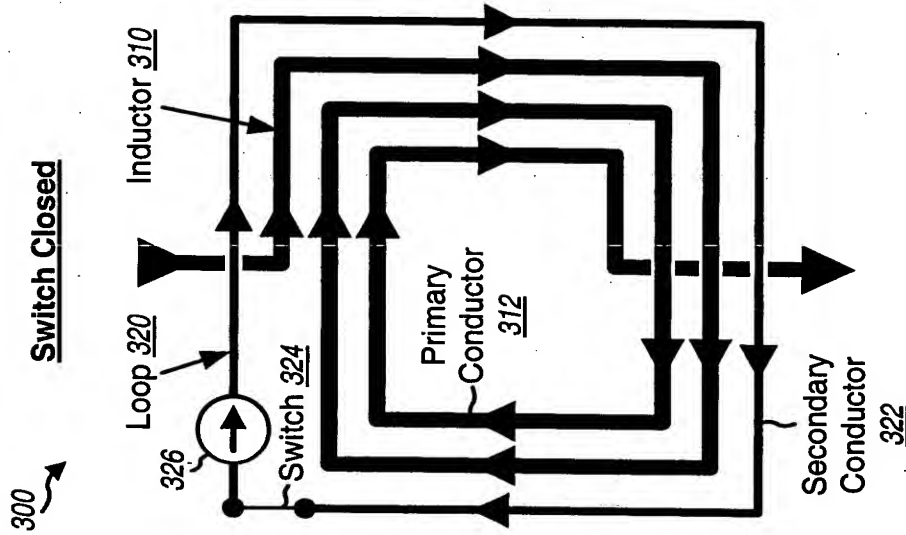


FIG. 3B

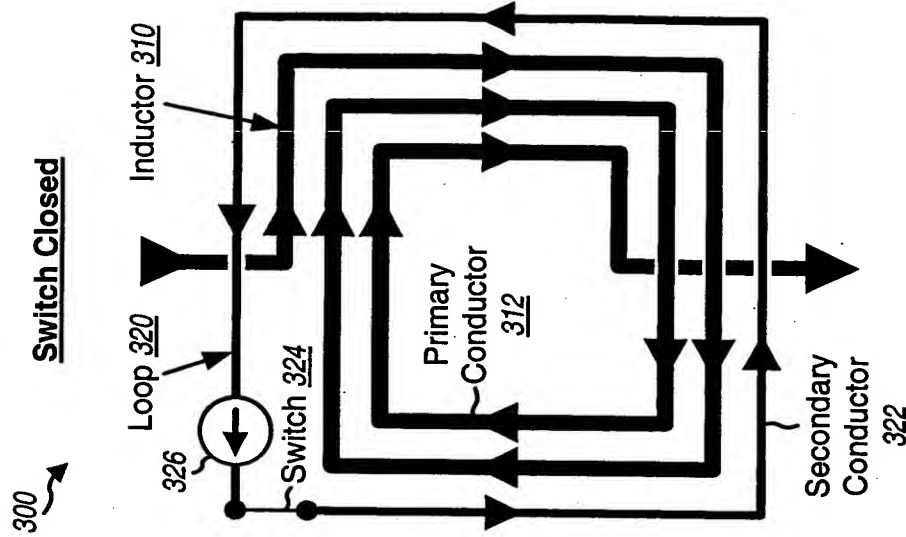


FIG. 3C

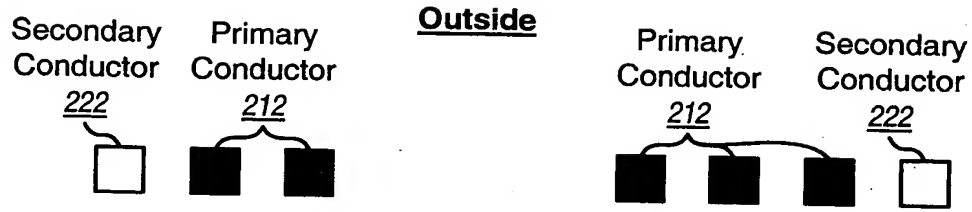


FIG. 4A

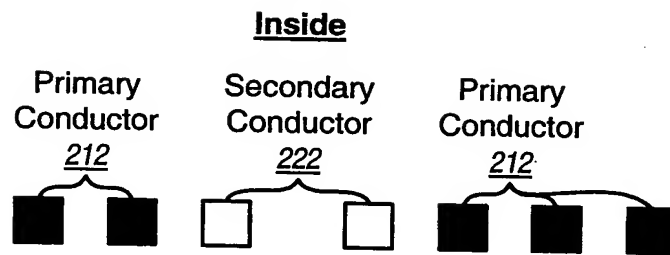


FIG. 4B

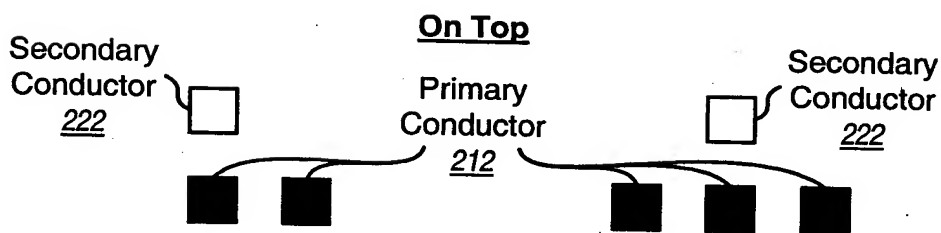


FIG. 4C

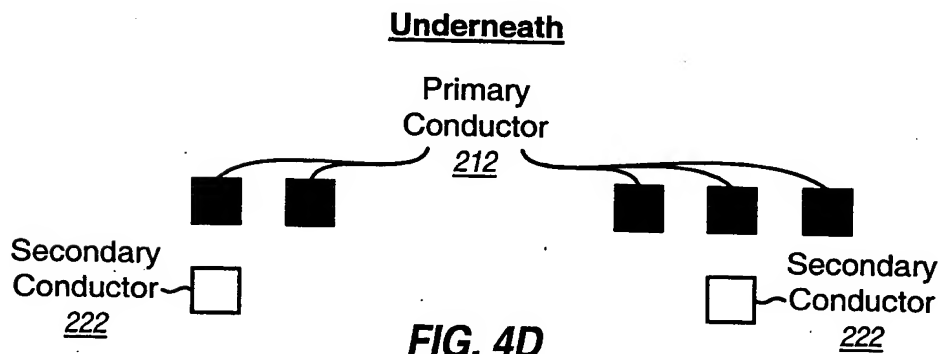


FIG. 4D

MULTI-LOOP PASSIVE CONFIGURATION

Switches Open

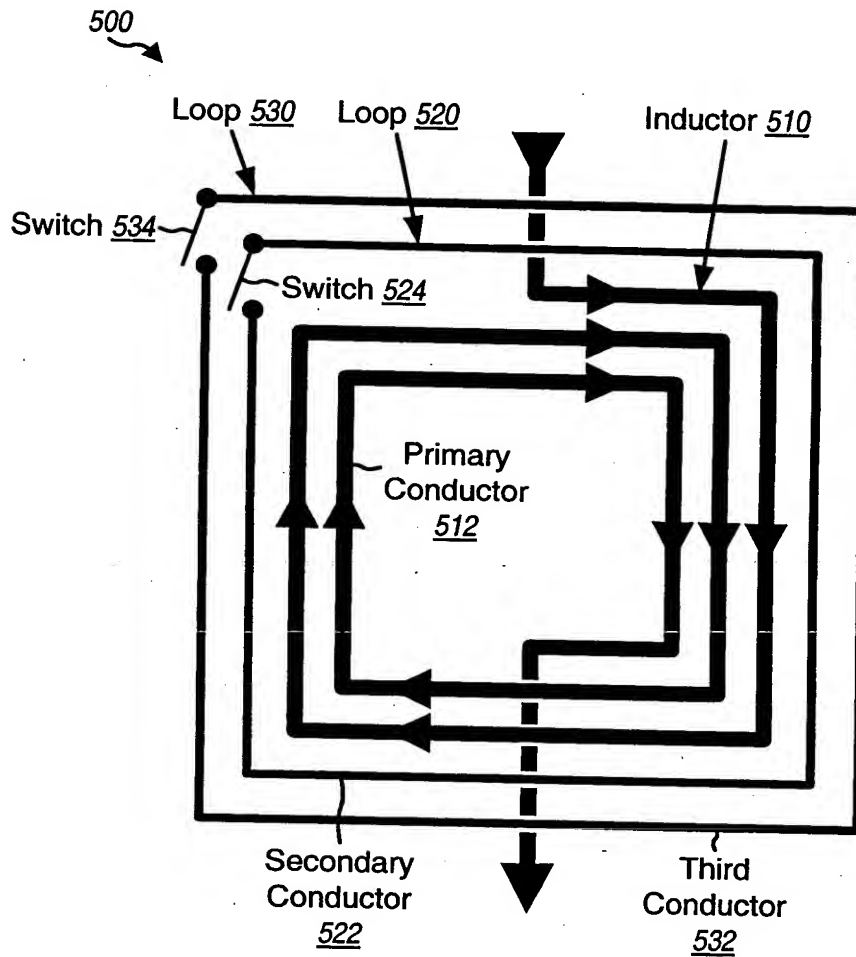


FIG. 5

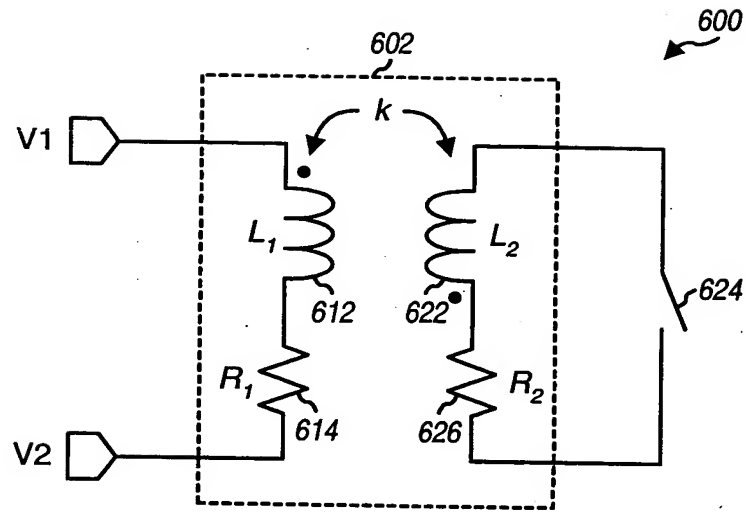


FIG. 6A

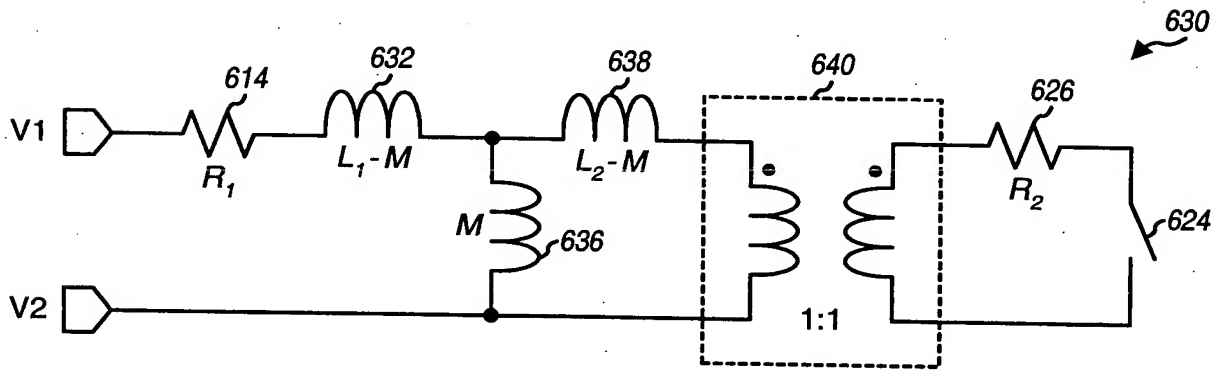


FIG. 6B

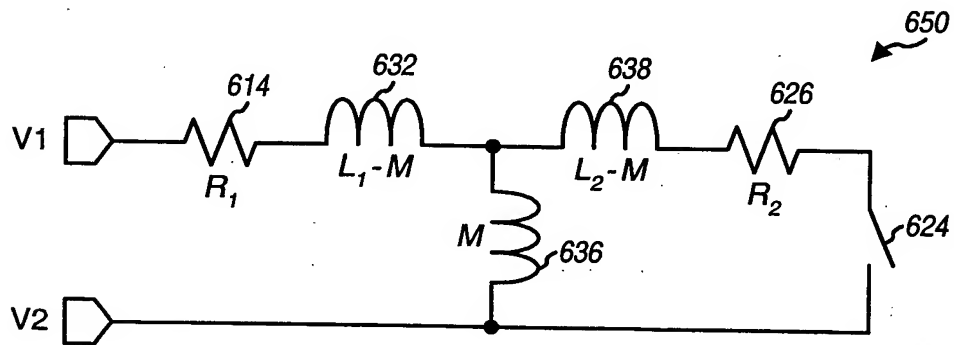


FIG. 6C

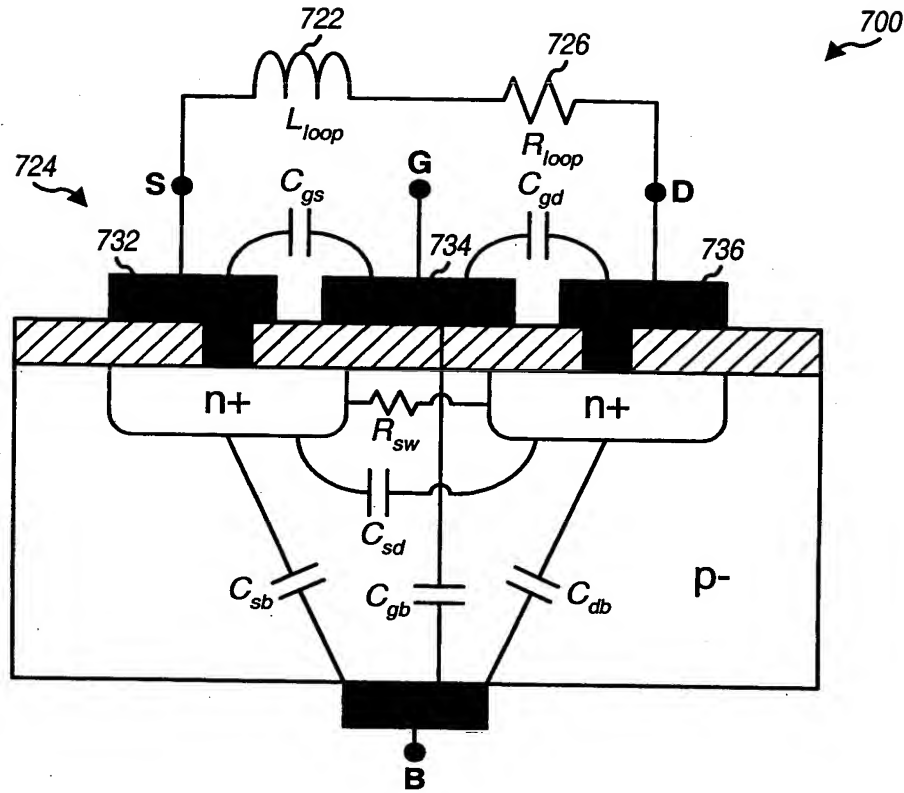


FIG. 7

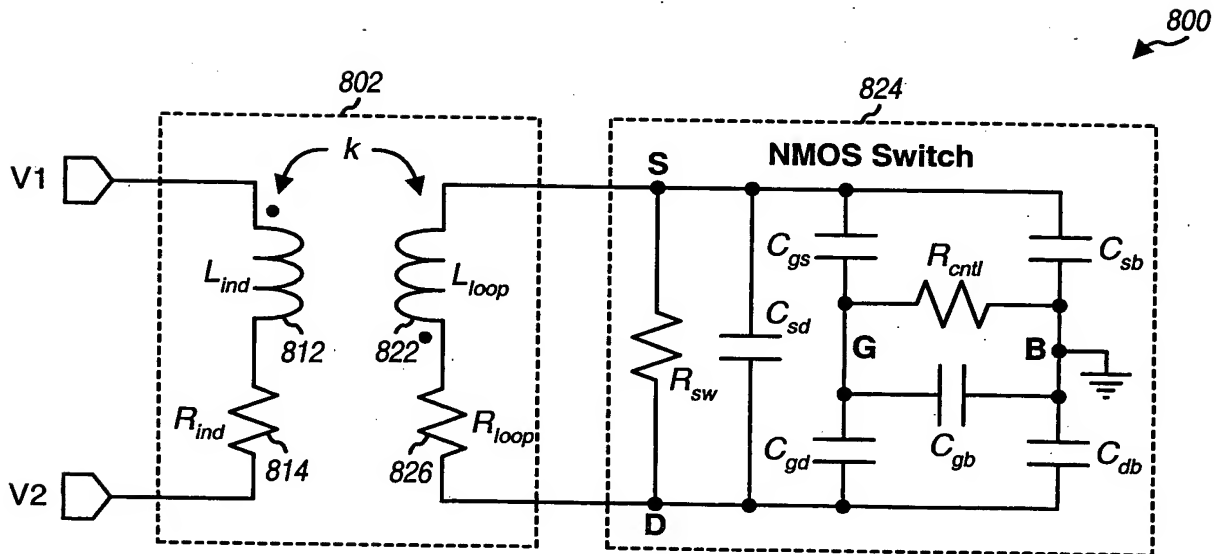
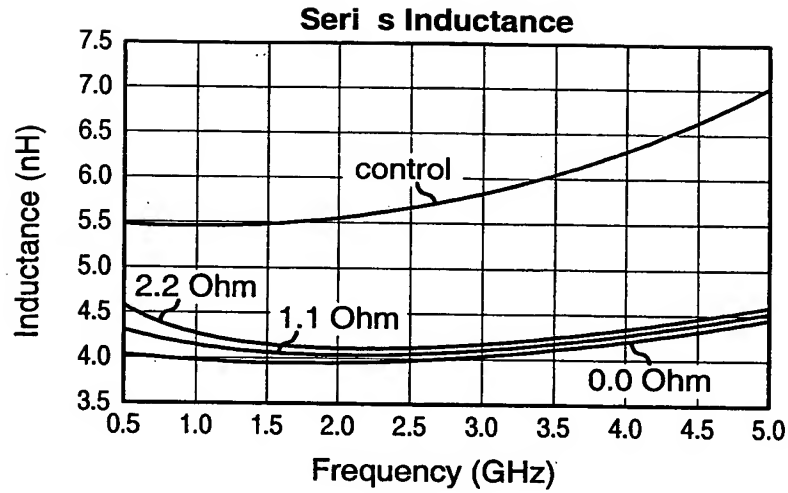
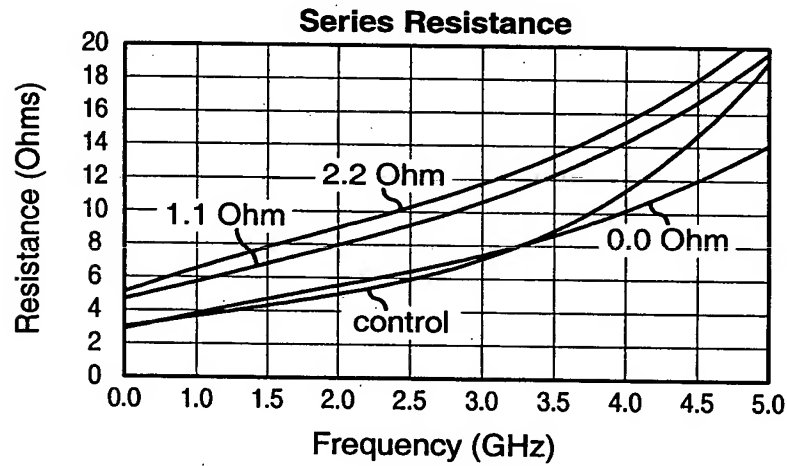
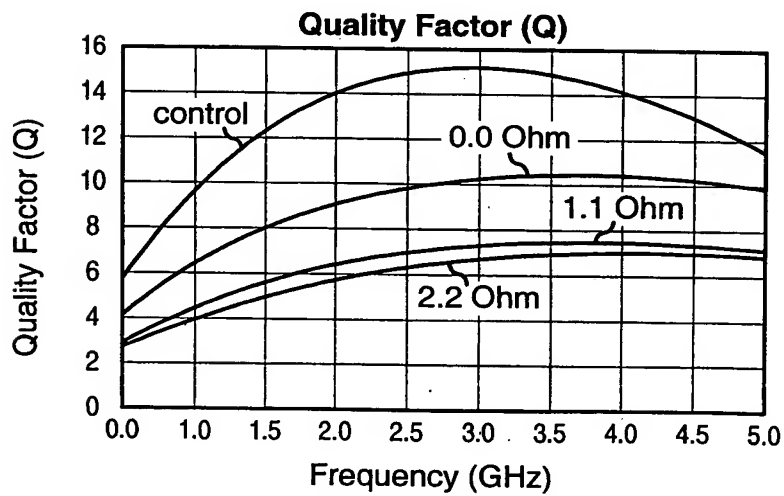


FIG. 8

**FIG. 9A****FIG. 9B****FIG. 9C**

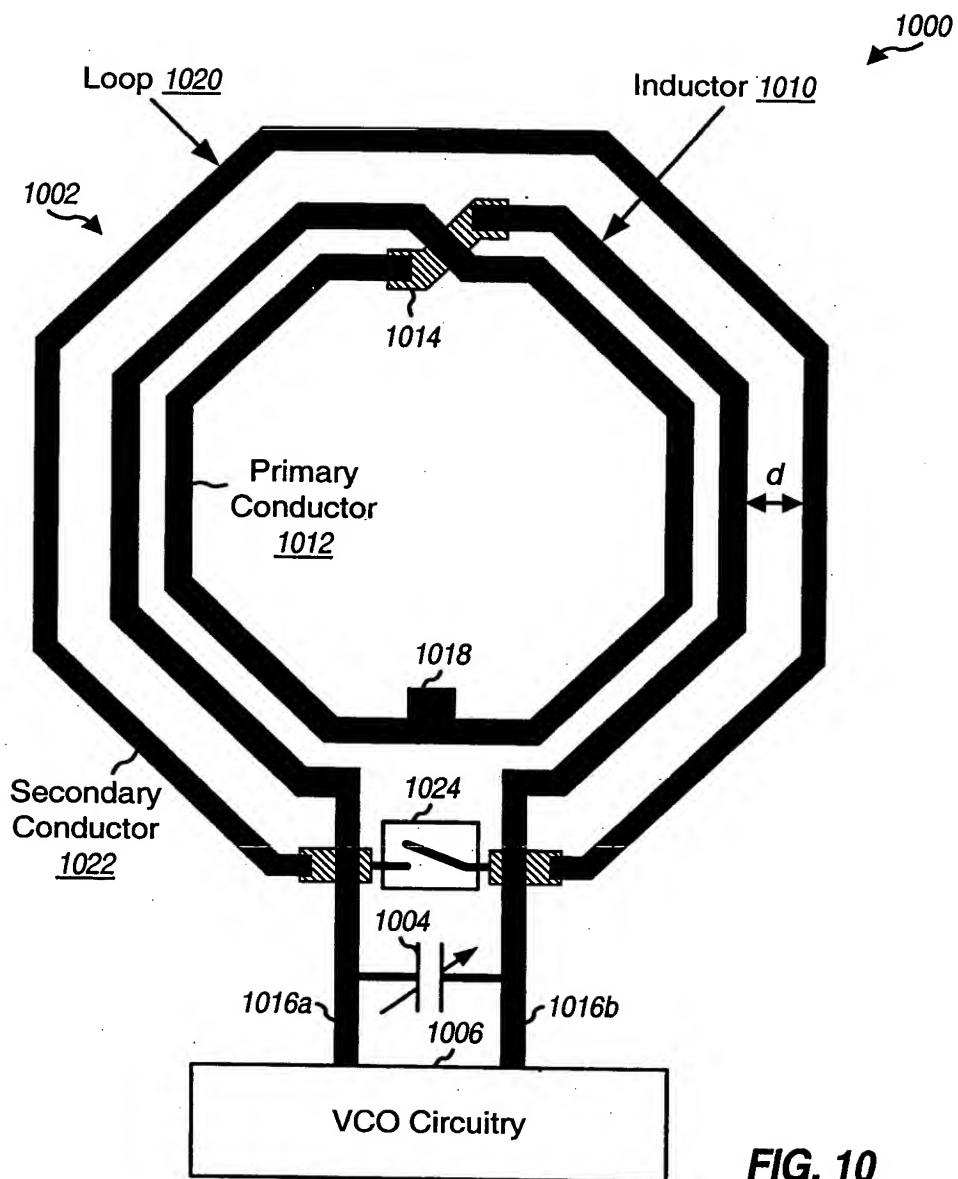


FIG. 10

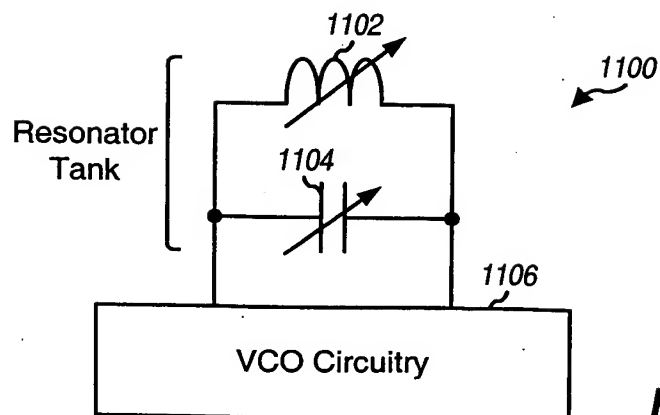


FIG. 11

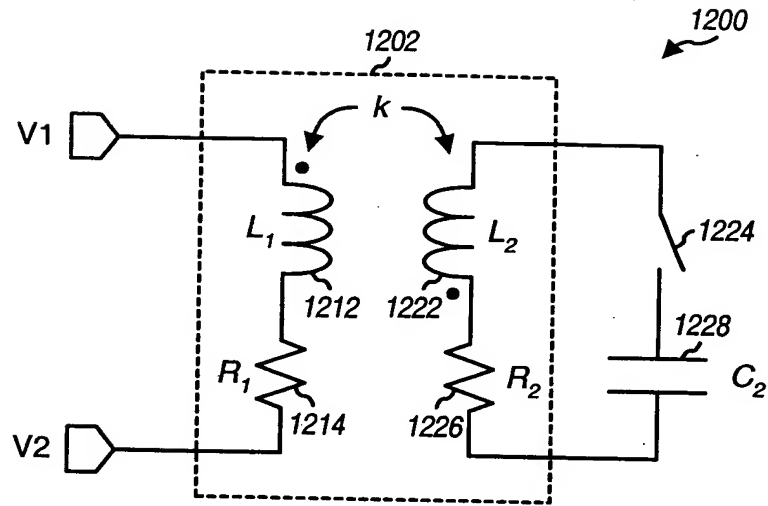


FIG. 12A

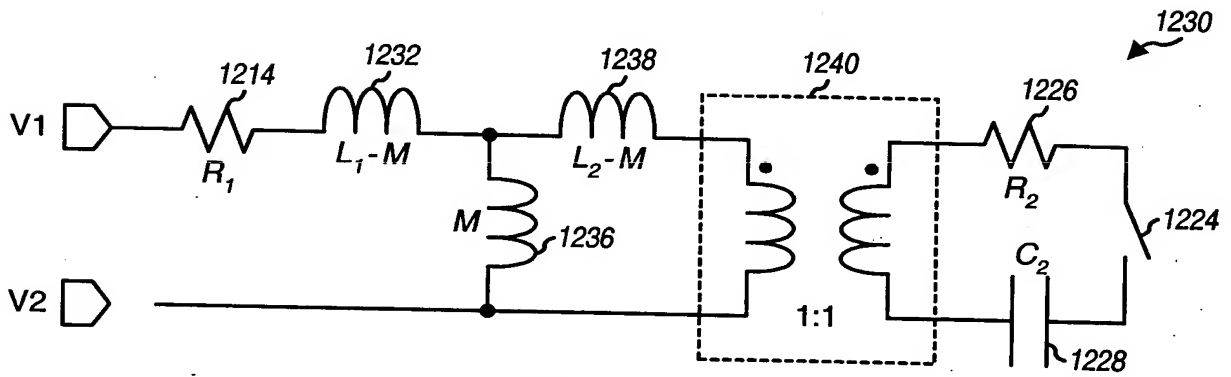


FIG. 12B

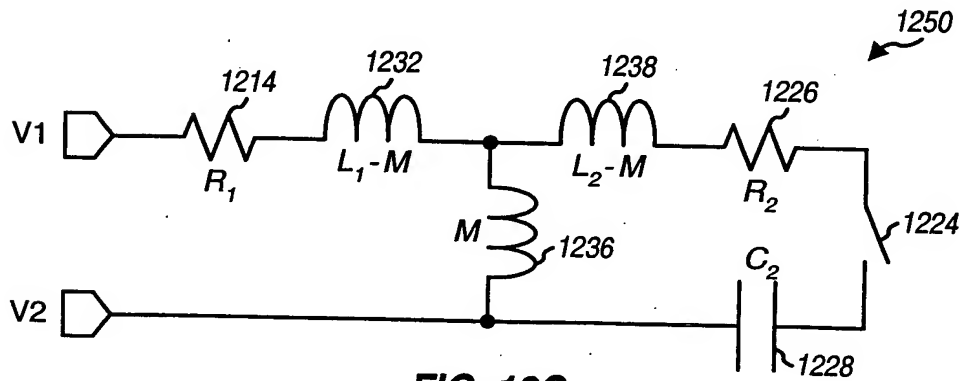
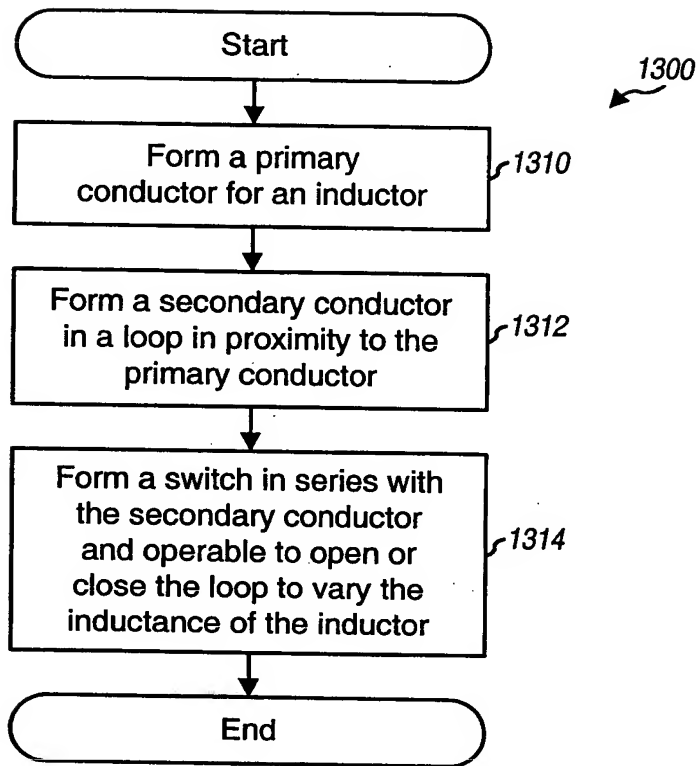


FIG. 12C

**FIG. 13**